FIG. 1

OUTPUT CIRCUIT

Vout

OUTPUT CIRCUIT

Vref

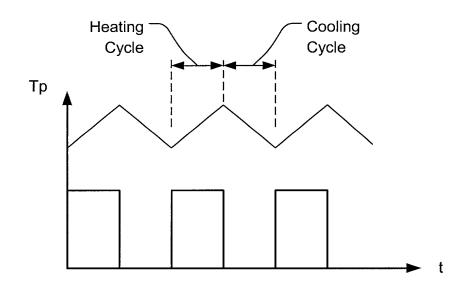


FIG. 2

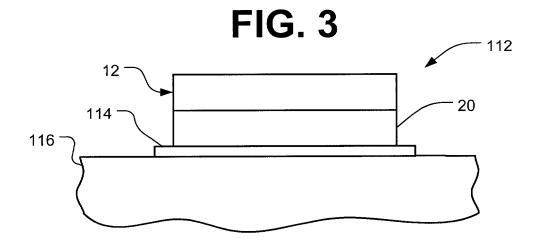


FIG. 5

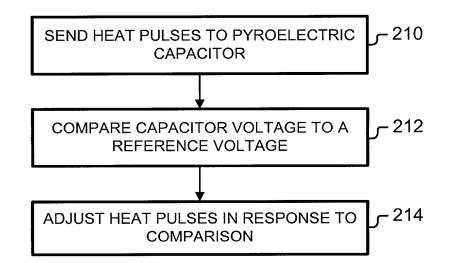


FIG. 4

